

# COMPUTER SCIENCE & ENGINEERING

## SECTION - A

*This question consists of 25 (TWENTY FIVE) multiple choice questions, each carrying one mark. For each question, four options are provided out of which exactly one is correct. Write the correct option for each question ONLY in the box provided for the question in the first sheet of the answer book.*

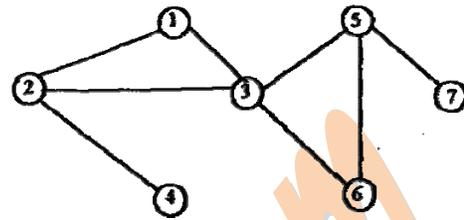
1. Suppose that the expectation of a random variable  $X$  is 5. Which of the following statements is true?
  - a. There is a sample point at which  $X$  has the value 5.
  - b. There is a sample point at which  $X$  has value greater than 5.
  - c. There is a sample point at which  $X$  has a value greater than or equal to 5.
  - d. None of the above.
2. The number of binary relations on a set with  $n$  elements is:
  - a.  $n^2$
  - b.  $2^n$
  - c.  $2^{n^2}$
  - d. None of the above
3. The number of binary strings of  $n$  zeroes and  $k$  ones such that no two ones are adjacent is:
  - a.  ${}^{n-1}C_k$
  - b.  ${}^nC_k$
  - c.  ${}^nC_{k+1}$
  - d. None of the above
4. Consider the regular expression  $(0\pm 1)(0+1) \dots n$  times. The minimum state finite automaton that recognizes the language represented by this regular expression contains:
  - a.  $n$  states
  - b.  $n+1$  states
  - c.  $n+2$  states
  - d. None of the above
5. Context free languages are closed under:
  - a. Union, intersection
  - b. Union, Kleene closure
  - c. Intersection, complement
  - d. Complement, Kleene Closure
6. Let  $L_D$  be the set of all languages accepted by a PDA by final state and  $L_E$  the set of all languages accepted by empty stack. Which of the following is true?
  - a.  $L_D = L_E$
  - b.  $L_D \supset L_E$
  - c.  $L_D \subset L_E$
  - d. None of these
7. Which of the following expressions is not equivalent to  $\bar{x}$ ?
  - a.  $x \text{ NAND } x$
  - b.  $x \text{ NOR } x$
  - c.  $x \text{ NAND } 1$
  - d.  $x \text{ NOR } 1$
8. Which of the following functions implements the Karnaugh map shown below?
 

$CD$	00	01	11	10
$AB$				
00	0	0	1	0
01	X	X	1	X
11	0	1	1	0
10	0	1	1	0

  - a.  $\bar{A}B + CD$
  - b.  $D(C + A)$
  - c.  $AD + \bar{A}B$
  - d.  $(C + D)(\bar{C} + D)(A + B)$
9. Listed below are some operating system abstractions (in the left column) and the hardware components or mechanism (in the right column) that they are abstractions of. Which of the following matching of pairs is correct?
 

<ol style="list-style-type: none"> <li>A. Thread</li> <li>B. Virtual address space</li> <li>C. File system</li> <li>D. Signal</li> </ol>	<ol style="list-style-type: none"> <li>A. Thread</li> <li>B. Virtual address space</li> <li>C. File system</li> <li>D. Signal</li> </ol>
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1. Interrupt
2. Memory
3. CPU
4. Disk
  - a. A-2, B-4, C-3, D-1
  - b. A-1, B-2, C-3, D-4
  - c. A-3, B-2, C-4, D-1
  - d. A-4, B-1, C-2, D-3
10. Which of the following disk scheduling strategies is likely to give the best throughput?
  - a. Farthest cylinder next
  - b. Nearest cylinder next
  - c. First come first served
  - d. Elevator algorithm
11. System calls are usually invoked by using:
  - a. A software interrupt
  - b. Polling
  - c. An indirect jump
  - d. A privileged instruction
12. A sorting technique is called stable if:
  - a. It takes  $O(n \log n)$  time
  - b. It maintains the relative order of occurrence of non-distinct elements
  - c. It uses divide and conquer paradigm
  - d. It takes  $O(n)$  space
13. Suppose we want to arrange the  $n$  numbers stored in an array such that all negative values occur before all positive ones. Minimum number of exchanges required in the worst case is:
  - a.  $n-1$
  - b.  $n$
  - c.  $n+1$
  - d. None of the above
14. If one uses straight two-way merge sort algorithm to sort the following elements in ascending order  
20, 47, 15, 8, 9, 4, 40, 30, 12, 17  
then the order of these elements after the second pass of the algorithm is:
  - a. 8,9,15,20,47,4,12,17,30,40
  - b. 8,15,20,47,4,9, 30,40,12,17
  - c. 15,20,47,4,8,9,12,30,40,17
  - d. 4,8,9,15,20,47,12,17,30,40
15. The number of articulation points of the following graph is:



- a. 0
  - b. 1
  - c. 2
  - d. 3
16. If  $n$  is a power of 2, then the minimum number of multiplications needed to compute  $a^n$  is:
    - a.  $\log_2 n$
    - b.  $\sqrt{n}$
    - c.  $n-1$
    - d.  $n$
  17. Which of the following is the most powerful parsing method?
    - a. LL(1)
    - b. Canonical ER
    - c. SER
    - d. EALR
  18. Consider the join of a relation  $R$  with a relation  $S$ . If  $R$  has  $m$  tuples and  $S$  has  $n$  tuples, then the maximum and minimum sizes of the join respectively are:
    - a.  $m+n$  and 0
    - b.  $mn$  and 0
    - c.  $m+n$  and  $m-n$
    - d.  $mn$  and  $m+n$
  19. The relational algebra expression equivalent to the following tuple calculus expression  
 $\{t \mid t \in r \wedge (t[A]=10 \wedge t[B]=20)\}$  is
    - a.  $\sigma(A=10 \vee B=20)(r)$
    - b.  $\sigma(A=10)(r) \cup \sigma(B=20)(r)$
    - c.  $\sigma(A=10)(r) \cap \sigma(B=20)(r)$
    - d.  $\sigma(A=10)(r) - \sigma(B=20)(r)$
  20. Booth's coding in 8 bits for the decimal number  $-57$  is:
    1. 0-100+1000
    2. 0-100+100-1
    3. 0-1+100-10+1

4.  $00-10+100-1$
21. The maximum gate delay for any output to appear in an array multiplier for multiplying two  $n$  bit numbers is:
- $O(n^2)$
  - $O(n)$
  - $O(\log n)$
  - $O(1)$
22. The main memory of a computer has  $2m$  blocks while the cache has  $2c$  blocks. If the cache uses the set associative mapping scheme with 2 blocks per set, then block  $k$  of the main memory maps to the set:
- $(k \bmod m)$  of the cache
  - $(k \bmod c)$  of the cache
  - $(k \bmod 2c)$  of the cache
  - $(k \bmod 2c.m)$  of the cache
23. The Newton-Raphson method is to be used to find the root of the equation  $f(x) = 0$  where  $x_0$  is the initial approximation and  $f'$  is the derivative of  $f$ . The method converges:
- Always
  - Only if  $f$  is a polynomial
  - Only if  $f(x_0) < 0$
  - None of the above
24. Let  $R = (A, B, C, D, E, F)$  be a relation scheme with the following dependencies:  $C \rightarrow F, E \rightarrow A, EC \rightarrow D, A \rightarrow B$ . Which of the following is a key for  $R$ ?
- CD
  - EC
  - AE
  - AC
25. Which of the following is correct?
- B-trees are for storing data on disk and  $B^+$  trees are for main memory.
  - Range queries are faster on  $B^+$  trees.
  - B-trees are for primary indexes and  $B^+$  trees are for secondary indexes.
  - The height of a  $B^+$  tree is independent of the number of records.
26. Consider two events  $E_1$  and  $E_2$  such that probability of  $E_1, \Pr[E_1] = 1/2$ , probability of  $E_2, \Pr[E_2] = 1/3$ , and probability of  $E_1$  and  $E_2, \Pr[E_1 \text{ and } E_2] = 1/5$ . Which of the following statements is/are true?
- $\Pr[E_1 \text{ or } E_2]$  is  $2/3$
  - Events  $E_1$  and  $E_2$  are independent
  - Events  $E_1$  and  $E_2$  are not independent
  - $\Pr(E_1/E_2) = 4/5$
27. Two girls have picked 10 roses, 15 sunflowers and 14 daffodils. What is the number of ways they can divide the flowers amongst themselves?
- 1638
  - 2100
  - 2640
  - None of the above
28. Let  $L$  be a set with a relation  $R$  which is transitive, anti-symmetric and reflexive and for any two elements  $a, b \in L$  let the least upper bound  $\text{lub}(a, b)$  and the greatest lower bound  $\text{glb}(a, b)$  exist. Which of the following is/are true?
- $L$  is a poset
  - $L$  is a boolean algebra
  - $L$  is a lattice
  - None of the above
29. If  $L_1$  is a context free language and  $L_2$  as a regular language which of the following is/are false?
- $L_1 - L_2$  is not context free
  - $L_1 \cap L_2$  is context free
  - $\neg L_1$  is context free
  - $\neg L_2$  is regular
30. Given the programming constructs i) assignment ii) for loops where the loop parameter cannot be changed within the loop iii) If-then-else iv) forward go to v) arbitrary go to vi) non-recursive procedure call vii) recursive procedure/function call viii) repeat loop, which constructs will you not include in a programming language such that it should be possible to program

(25 × 2 = 50)

***This question consists of 25 (Twenty five) multiple choice questions, each carrying 2 marks. For each question, 4 options are provided out of which one or more are correct. Write ALL the correct options for each question only in the***

the terminates (i.e. halting) function in the same programming language.

- ii), iii), iv)
- v), vii) viii)
- vi), vii), viii)
- iii), vii), viii)

31. For the schedule given below, which of the following is correct:

```

1 Read A
2           Read B
3 Write A
4           Read A
5           Write A
6           Write B
7 Read B
8 Write B

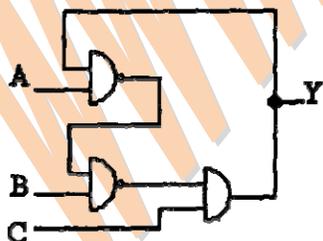
```

- This schedule is serialisable and can occur in a scheme using 2PL protocol.
- This schedule is serialisable but cannot occur in a scheme using 2PL protocol
- This schedule is not serialisable but can occur in a scheme using 2PL protocol
- This schedule is not serialisable and cannot occur in a scheme using 2PL protocol.

32. Consider the schema  $R = (S \ T \ U \ V)$  and the dependencies  $S \rightarrow T$ ,  $T \rightarrow U$ ,  $U \rightarrow V$  and  $V \rightarrow S$ . Let  $R$  ( $R_1$  and  $R_2$ ) be a decomposition such that  $R_1 \cap R_2 \neq \emptyset$ . The decomposition is:

- Not in 2NF
- In 2NF but not in 3NF
- In 3NF but not in 2NF
- In both 2NF and 3NF

33. Consider the circuit shown below. In a certain steady state, the line Y is at '1' What are the possible values of A, B, and C in this state?



- $A = 0, B = 0, C = 1$
- $A = 0, B = 1, C = 1$
- $A = 1, B = 0, C = 1$
- $A = 1, B = 1, C = 1$

34. Which of the following sets of component(s) is/are sufficient to implement any arbitrary boolean function?

- XOR gates, NOT gates
- 2 to 1 multiplexors
- AND gates, XOR gates
- Three-input gates that output  $(A.B)+C$  for the inputs A, B, and C.

35. A multi-user, multi-processing operating system cannot be implemented on hardware that does not support:

- Address translation.
- DMA for disk transfer.
- At least two modes of CPU execution (privileged and non-privileged).
- Demand paging.

36. Which of the following is/are advantages of virtual memory?

- Faster access to memory on an average.
- Processes can, be given protected address spaces.
- Linker can assign addresses independent of where the program will be loaded in physical memory.
- Programs larger than the physical memory size can be run.

37. Which of the following actions is/are typically not performed by the operating system when switching context from process A to process B?

- Saving current register values and restoring saved register values for process B.
- Changing address translation tables.
- Swapping out the memory image of process A to the disk.
- Invalidating the translation look-aside buffer

38. Consider the following program in a language that has dynamic scoping.

```

var x : real;
procedure show;
  begin print (x); end;
procedure small;
  var x : real;
    begin x := 0.125; show; end;
begin x := 0.25;
  show; small;
end.

```

Then the output of the program is:

- a. 0.125 0.125  
 b. 0.25 0.25  
 c. 0.25 0.125  
 d. 0.125 0.25
39. The number of tokens in the Fortran statement DO 10 I = 1.25 is  
 a. 3  
 b. 4  
 c. 5  
 d. None of the above
40. A grammar that is both left and right recursive for a non-terminal, is  
 a. Ambiguous  
 b. Unambiguous  
 c. Information is not sufficient to decide whether it is ambiguous or unambiguous.  
 d. None of the above
41. The number of full and half-adders required to add 16-bit numbers is:  
 a. 8 half-adders, 8 full-adders  
 b. 1 half-adder, 15 full-adders  
 c. 16 half-adders, 0 full-adders  
 d. 4 half-adders, 12 full-adders
42. Zero has two representations in:  
 a. Sign magnitude  
 b. 1's complement  
 c. 2's complement  
 d. None of the above
43. RAID configurations of disks are used to provide  
 a. Fault-tolerance  
 b. High speed  
 c. High data density  
 d. None of the above
44. Arrange the following configurations for CPU in decreasing order of operating speeds;  
 Hardwired control, vertical micro-programming, horizontal microprogramming  
 a. Hardwired control, vertical micro-programming, horizontal micro-programming  
 b. Hardwired control, horizontal micro-programming, vertical micro-programming  
 c. Horizontal micro-programming, vertical micro-programming, hardwired control  
 d. Vertical micro-programming, horizontal micro-programming, hardwired control
45. The minimum number of record movements required to merge five files A (with 10 records), B (with 20 record), C (with 15 records), D (with 5 records) and E (with 25 records) is:  
 a. 165  
 b. 90  
 c. 75  
 d. 65
46. If  $T_1 = O(1)$ , give the correct matching for the following pairs:  

(M) $T_n = T_{n-1} + n$	(U) $T_n = O(n)$
(N) $T_n = T_{n/2} + n$	(V) $T_n = O(n \log n)$
(O) $T_n = T_{n/2} + n \log n$	(W) $T_n = O(n^2)$
(P) $T_n = T_{n/2} \cdot \log n$	(X) $T_n = O(\log^2 n)$

 a. M-W, N-V, O-U, P-X  
 b. M-W, N-U, O-V, P-V  
 c. M-V, N-W, O-X, P-U  
 d. M-W, N-U, O-V, P-X
47. The main difference(s) between a CISC and a RISC processor is/are that a USC processor typically:  
 a. has fewer instructions  
 b. has fewer addressing modes  
 c. has more registers  
 d. is easier to implement using hard-wired control logic
48. A certain processor supports only the immediate and the direct addressing modes. Which of the following programming language features cannot be implemented on this processor?  
 a. Pointers  
 b. Arrays  
 c. Records  
 d. Recursive procedures with local variables
49. Consider the following C function dJinition.

```

int Trial(int a, int b, int c)
{
    if ((a >= b) && (c < b)) return b;
    else if (a >= b) return Trial(a,c,b);
    else return Trial(b,a,c);
}

```

The function Trial:

- finds the maximum of a, b, and c
- finds the minimum of a, b, and c
- finds the middle number of a, b, and c
- None of the above

50. Which of the following is/are correct?

- An SQL query automatically eliminates duplicates
- An SQL query will not work if there are no indexes on the relations
- SQL permits attribute names to be repeated in the same relation
- None of the above

## SECTION - B

*This section consists of TWENTY questions of FIVE marks each. ANY FIFTEEN out of them have to be answered. If more number of questions is attempted, score off the answers not to be evaluated, else only the first fifteen unscored answers will be considered.*

51.

- Mr. X claims the following:  
If a relation R is both symmetric and transitive, then R is reflexive. For this, Mr. X offers the following proof

“From  $xRy$ , using symmetry we get  $yRy$ . Now because R is transitive,  $xRy$  and  $yRy$  together imply  $xRx$ . Therefore, R is reflexive.”

Briefly point out the flaw in Mr. X's proof.

(3)

- Give an example of a relation R which is symmetric and transitive but not reflexive.

(2)

52. Let G be a finite group and H be a subgroup of G. For  $a \in G$ , define  $aH = \{ah \mid h \in H\}$ .

- Show that  $|aH| = |H|$

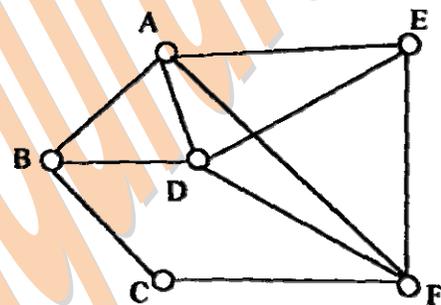
- Show that for every pair of elements  $a, b \in G$ , either  $aH = bH$  or  $aH$  and  $bH$  are disjoint.

(2)

- Use the above to argue that the order of H must divide the order of G.

(2)

53. Let G be a connected, undirected graph. A cut in G is a set of edges whose removal results in G being broken into two or more components which are not connected with each other. The size of a cut is called its cardinality. A min-cut of G is a cut in G of minimum cardinality. Consider the following graph.



- Which of the following sets of edges is a cut?

(i)  $\{(A,B), (E,F), (B,D), (A,E), (A,D)\}$

(ii)  $\{(B,D), (C,F), (A,B)\}$

(1)

- What is the cardinality of a min-cut in this graph?

(2)

- Prove that if a connected undirected graph G with n vertices has a min-cut of cardinality k, then G has at least  $(nk/2)$  edges

(2)

54.

- Given that A is regular and  $A \cup B$  is regular, does it follow that B is necessarily regular? Justify your answer.

(2)

- Given two finite automata  $M_1, M_2$ , outline an algorithm to decide if  $L(M_1) \subsetneq L(M_2)$ . (note: strict subset).

(3)

55. Show that the language  $L = \{xcx \mid x \in \{0,1\}^*$  and  $c$  is a terminal symbol} is not context free  $c$  is not 0 or 1.

(5)

56. Let  $A$  be an  $n \times n$  matrix such that the elements in each row and each column are arranged in ascending order. Draw a decision tree which finds 1<sup>st</sup>, 2<sup>nd</sup> and 3<sup>rd</sup> smallest elements in minimum number of comparisons.

(5)

57. Let synthesized attribute val give the value of the binary number generated by  $S$  in the following grammar. For example, on input 101.101,  $S.val = 5.625$ .

$S \rightarrow LL \mid L$   
 $L \rightarrow LB \mid B$   
 $B \rightarrow 0 \mid 1$

Write S-attributed values corresponding to each of the productions to find  $S.val$ .

(5)

58. Suppose we have a function HALTS which when applied to any arbitrary function  $f$  and its arguments will say TRUE if function  $f$  terminates for those arguments and FALSE otherwise. Example: Given the following function definition.

FACTORIAL (N) = IF (N=0) THEN 1  
 ELSE N\*FACTORIAL (N- 1)

Then HALTS(FACTORIAL 4) = TRUE  
 and HALTS(FACTORIAL -5) = FALSE

Let us define the function FUNNY ( $f$ ) = IF HALTS( $ff$ ) THEN not( $ff$ ) ELSE TRUE

a. Show that FUNNY terminates for all functions  $f$

(2)

b. Use (a) to prove (by contradiction) that it is not possible to have a function like HALTS which for arbitrary functions and inputs says whether it will terminate on that input or not.

(3)

59.

a. Consider the following algorithm. Assume, procedure A and procedure B take  $O(1)$  and  $O(1/n)$  unit of time respectively. Derive the time complexity of the algorithm in  $O$ -notation.

```

algorithm what(n)
begin
    if n = 1 then call A
    else begin
        what(n-1);
        call B(n)
    end
end
    
```

b. Write a constant time algorithm to insert a node with data  $D$  just before the node with address  $p$  of a singly linked list.

(2)

60.

a. In a binary tree, a nil node is defined to be a node with 2 children. Use induction on the height of the binary tree to prove that the number of full nodes plus one is equal to the number of leaves.

(3)

b. Draw the mm-heap that results from insertion of the following elements in order into an initially empty mm-heap: 7, 6, 5, 4, 2, 3, 1. Show the result after the deletion of the root of this heap.

(2)

61.

An instruction pipeline consists of 4 stages: Fetch (F), Decode operand field (D), Execute (E), and Result-Write (W). The 5 instructions in a certain instruction sequence need these stages for the different number of clock cycles as shown by the table below.

Instruction	No. of clock cycles needed for			
	F	D	E	W
1	1	2	1	1
2	1	2	2	1
3	2	1	3	2
4	1	3	2	1
5	1	2	1	2

Find the number of clock cycles needed to perform the 5 instructions.

(5)

62.

a. Show that the formula  $[(\sim p \vee q) \Rightarrow (q \Rightarrow p)]$  is not a tautology.

(3)

b. Let  $A$  be a tautology and  $B$  be any other formula Prove that  $(A \vee B)$  is a tautology.

63. What will be the output of the following program assuming that parameter passing is

- (i) call by value
- (ii) call by reference
- (iii) call by copy restore

```

procedure P(x,y,z),
  begin y := y+1; z := z+x end;
begin
  a := 2; b := 3;
  P(a+b, a, a);
  print (a)
end.

```

(5)

64. Consider the following Pascal program skeleton:

```

program sort(...),
  var a, x, ...;
  procedure readarray;
    var i, ...;
    begin
      ... := ... a ...
    end;
  procedure exchange(...);
    begin
      ... := ... a ...;
      ... := ... x ...;
    end;
  procedure qsort(...);
    var k, v, ...;
    function partition(...) ...;
      var i, j, ...;
      begin
        ... := ... a ...;
        ... := ... v ...;
      end;
    begin
      :
    end;
  begin
    :
  end.

```

Assume that at a given point in time during program execution, following procedures are active: sort, qsort(1, 9), qsort(1, 3), partition(1,3), exchange(1,3)

Show snapshots of the runtime stack with access Links after each of the activations.

(5)

65. Consider the following program fragment in the assembly language of a certain hypothetical processor. The processor has three general purpose registers R1, R2 and R3. The meanings of the instructions are

shown by comments (starting with) after the instructions.

```

X:  CMP R1, 0 ; Compare R1 and 0, set flags appropriately in status register.
    JZ Z ; Jump if zero to target Z.
    MOV R2, R1 ; Copy contents of R1 to R2.
    SHR R1 ; Shift right R1 by 1 bit.
    SHL R1 ; Shift left R1 by 1 bit.
    CMP R2, R1 ; Compare R2 and R1 and set flags in status register.
    JZ Y ; Jump if zero to target Y.
Y:  INC R3 ; Increment R3 by 1.
    SHR R1 ; Shift right R1 by 1 bit.
    JMP X ; Jump to target X.
Z:  ...

```

a. Initially, R1, R2, and R3 contain the values 5, 0, and 0 respectively. What are the final values of R1 and R3 when control reaches Z?

(2)

b. In general, if R1, R2, and R3 initially contain the values n, 0, and 0 respectively, what is the final value of R3 when control reaches Z?

(3)

66. Design a  $2K \times 8$  (2048 locations, each 8 bit wide) memory system mapped at addresses  $(1000)_{16}$  to  $(17FF)_{16}$  for the 8085 processor using four  $1K \times 4$  memory chips. Each of these chips has the following signal pins:

1.  $\overline{CS}$  (Chip select, data lines are in high impedance state when it is 1)
2.  $\overline{RD}$  (0 for read operation)
3.  $\overline{WR}$  (0 for write operation)
4.  $A_0, A_1, \dots, A_9$  (input address lines,  $A_0$  is the least significant)
5.  $D_0, D_1, D_2, D_3$  (bi-directional data lines,  $D_0$  is the least significant)

(5)

67. A certain computer system has the segmented paging architecture for virtual memory. The memory is byte addressable. -Both virtual and physical address spaces contain 216 bytes each. The virtual address space is divided into 8 non-overlapping equal size segments. The memory management unit (MMU) has a hardware -segment table, each entry of which contains the physical address of the page table for the segment. Page tables are stored in the main memory and consist of 2 byte page table entries.

- a. What is the minimum page size in bytes so that the page table for a segment requires at most one page to store it? Assume that the page size can only be power of 2.

- (2)
- b. Now suppose that the page size is 512 bytes. It is proposed to provide a TLB (translation look-aside buffer) for speeding up address translation. The proposed TLB will be capable of storing page table entries for 16 recently referenced virtual pages, in a fast cache that will use the direct mapping scheme. What is the number of tag bits that will need to be associated with each cache entry?

(1)

- c. Assume that each page table entry contains (besides other information) 1 valid bit, 3 bits for page protection and 1 dirty bit. How many bits are available in a page table entry for storing the aging information for the page? Assume that the page size is 512 bytes.

(2)

68.

- a. A certain processor provides a “test and set” instruction that is used as follows.

TSET register, flag

This instruction atomically copies flag to register and sets flag to 1. Give pseudo-code for implementing the entry and exit code to a critical region using this instruction.

(2)

- b. Consider the following solution to the producer-consumer problem using a buffer of size 1. Assume that the initial value of count is 0. Also assume that the testing of count and assignment to count are atomic operations.

**Producer:****Repeat**

**Produce an item;**  
**if count = 1 then sleep;**  
**Place item in buffer;**  
**count = 1;**  
**wakeup(Consumer);**

**Forever,****Consumer:****Repeat**

**if count = 0 then sleep;**  
**Remove item from buffer;**  
**count = 0;**  
**wakeup(Producer);**  
**Consume item;**

**Forever,**

Show that in this solution it is possible that both the processes are sleeping at the same time.

(3)

69. Consider a B-tree with degree  $m$ , that is, the number of children,  $c$ , of any internal node (except the root) is such that  $m \leq c \leq 2m - 1$ . Derive the maximum and minimum number of records in the leaf nodes for such a B-tree with height  $h$ ,  $h \geq 1$ . (Assume that the root of a tree is at height 0.)

(5)

70. Consider the set of relations  
 EMP(Employee-no, Dept-no, Employee-name; Salary)  
 DEPT(Dept-no, Dept-name, Location)  
 Write an SQL query to:

- a. Find all employee names who work in departments located at “Calcutta” and whose salary is greater than Rs. 50,000.
- b. Calculate, for each department number, the number of employees with a salary greater than Rs. 100,000.

(2)

(3)